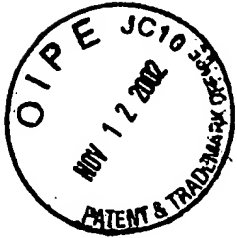


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2186
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11-14-02
November 12, 2002

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Technology Center 2100

Box NON-FEE AMENDMENT
Commissioner For Patents
Washington, D.C. 20231

Re: Application No.: 09/347,106
First Inventor: Hronik, Stanley A.
Group Art Unit: 2186
Atty. Docket No.: M-7086 US
Title: Double Data Rate Synchronous SRAM with 100% Bus Utilization
Assignee: Integrated Device Technology, Inc.

Confirmation No.: 3360
Filing Date: July 2, 1999
Examiner: Matthew D. Anderson

Dear Sir:

Transmitted herewith are the following documents in the above-identified application:

- (1) Return Receipt Postcard;
- (2) This Transmittal Letter; and
- (3) Response to Office Action, including Attachment A (18 pgs.).

☒ The fee has been calculated as shown below:

CLAIMS AS AMENDED

	Claims Remaining <u>After Amendment</u>		Highest No. Previously <u>Paid For</u>		Present <u>Extra</u>	<u>Rate</u>	<u>Additional Fee</u>
Total Claims	55	Minus	56	=	0	x \$18.00	\$
Independent Claims	5	Minus	6	=	0	x \$84.00	\$
<input type="checkbox"/>	Fee of \$___ for the first filing of one or more multiple dependent claims						\$
<input type="checkbox"/>	Fee for Request for Extension of Time (___ month(s))						\$
<input type="checkbox"/>	Fee for						\$
Total additional fee for this Amendment:							\$

- ☒ Please charge our Deposit Account No. 19-2386 in the amount of \$
- ☒ Also, charge any additional fees required and credit any overpayment to our Deposit Account No. 19-2386.

EXPRESS MAIL LABEL NO:

EV 259 160 113 US

Respectfully submitted,

Robert D. Wasson

Robert D. Wasson
Reg. No. 40,218